

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Yangling Sun



TITLE Phase-Locked Loop Circuit As Well As A Voltage-Controlled Oscillator As Used In A Phase-Locked Loop Circuit

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

NEW APPLICATION UNDER 37 CFR § 1.53(b)

Enclosed are the following papers relating to the above-named application for patent:

12 Specification

1 Sheet of Formal Drawing(s)

Information Disclosure Statement with references

CLAIMS AS FILED				
	NO. FILED	NO. EXTRA	RATE	CALCULATIONS
Total Claims	9- 20 =	0	x \$18 =	\$0
Independent Claims	1-3=	0	x \$80 =	\$0
Multiple Dependent Claims, if applicable			+ \$260 =	\$0
Basic Fee				\$710.00
	uhus 1		TOTAL FEE	\$710.00

Please file the application and charge **Agere Systems Deposit Account No. 501735** the amount of \$710.00, to cover the filing fee. Duplicate copies of this letter are enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 501735** as required to correct the error.

The Assistant Commissioner for Patents is hereby authorized to treat any concurrent or future reply, requiring a petition for extension of time under 37 CFR § 1.136 for its timely submission, as incorporating a petition for extension of time for the appropriate length of time if not submitted with the reply.

Please address all correspondence to **Docket Administrator**, **Agere Systems Inc.**, **P.O. Box 614**, **Berkeley Heights**, **New Jersey 07922-0614**. However, telephone calls should be made to me at 610-712-3753.

Respectfully,

Mark A. Kursiko, Attorney for Applicant

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"Express Mail" mailing label number ET249422092US

Date of Deposit 42801

I hereby certify that this Application
being deposited with the United States Postal
Service "Express Mail Post Office to

Addresses" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks

Washington, D. C. 20231.

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